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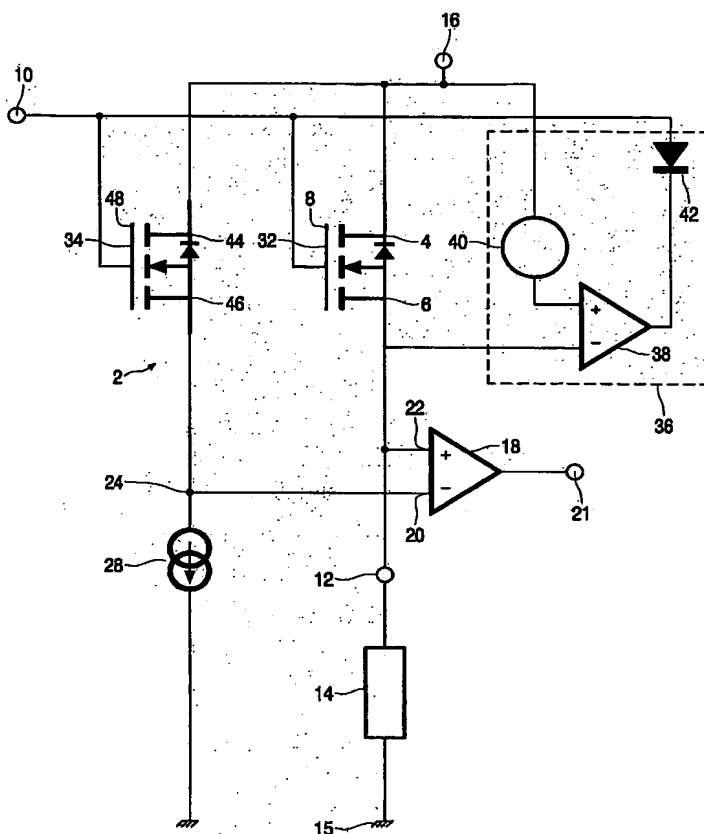
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**(54) Title:** UNDERCURRENT SENSE ARRANGEMENT AND METHOD



**(57) Abstract:** An on-state low current detector uses a transistor with main (32) and sense (34) cells. Feedback circuit (36) acts to keep the voltage across main cells (32) at a substantially constant target value when the load current falls below a level that generates the target voltage value in the main cells. The target voltage value is sufficiently high to ensure that the voltages of low current detection comparator (18) are readily measurable.

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